

Problem C: GPU Accelerated Logic Rewriting

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Q&A

Q1. In the evaluation part, the script are scored based on the **run-time** and **GPU utility**. Any requirement about **QoR** of the output circuit of our implementation? Are we allowed to change the methodology of rewrite (e.g. change the traversal order, cut enumeration algorithms, ...), which will probably result in a different QoR?

A1. No change to rewrite other than poring it to CUDA is allowed and so QoRs should remain the same.